

# Alliacense®

a TPL Group Enterprise

## FACSIMILE TRANSMITTAL SHEET

TO:	Tetsujuki Watanabe	FROM:	Nicole Langford
COMPANY:	MEI	DATE:	12 JUL 2005
FAX NUMBER:	011-81-66-949-4545	TOTAL NO. OF PAGES INCLUDING COVER:	7
PHONE NUMBER:		SENDER'S REFERENCE NUMBER:	
RE:	MEI - TPL MMP Portfolio	YOUR REFERENCE NUMBER:	

☐ URGENT ☐ FOR REVIEW ☐ PLEASE COMMENT ☐ PLEASE REPLY ☐ PLEASE RECYCLE

NOTES/COMMENTS:

Mr. Watanabe,

The following materials were Fed Ex'd to you on Tuesday 11 July tracking number 790573000350.

1. MEI - TPL MMP Portfolio v.2 Binder
2. MEI - TPL MMP Portfolio Disk

I am also enclosing via fax a copy of the Table of Contents and background information on the MMP Portfolio.

Please advise receipt of the package via email to [nicole@tplgroup.net](mailto:nicole@tplgroup.net), [sanjose@tplgroup.net](mailto:sanjose@tplgroup.net), and [mac@tplgroup.net](mailto:mac@tplgroup.net).

Received

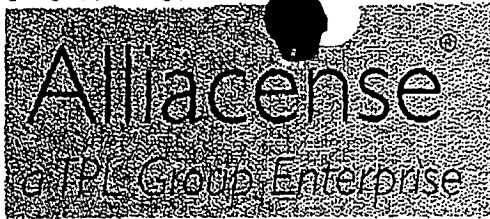
JUL 13 2005

Matsushita/IPROC

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11 JUL 05

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IMPORTANT NEWS ABOUT THE  
MOORE MICROPROCESSOR PATENT ("MMP") PORTFOLIO

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Dear Mr. Watanabe

The MMP Portfolio was originally brought to your attention by Patriot Scientific Corporation and its lawyers early in 2004. You need to be aware of several significant events that have occurred since then.

I. TPL GROUP: EXCLUSIVE MANAGER OF MMP PORTFOLIO LICENSING PROGRAM

The TPL Group now has exclusive control over all Licensing and Litigation with respect to the MMP Portfolio. Neither Patriot nor its lawyers are authorized to communicate with you regarding the MMP Portfolio.

II. INDUSTRY-LEADING LICENSEES

Unlike many disputed or questionable patents, it has been widely acknowledged that the MMP Portfolio contains several fundamental building blocks of modern microprocessor architecture and implementation.

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As a testament to this practical reality, and in response to their requests to be first, industry leaders Intel and AMD each recently purchased MMP Portfolio licenses to cover their products, and to protect their corporate treasuries from massive exposure.

Going forward, the MMP Portfolio Licensing Program will reward first movers in their industry sectors with dramatic discounts. Those who fail to act will pay much more than their competitors, or will eventually face costly litigation. By design, this structure enables nimble and forward-thinking companies to disadvantage their competitors.

## II. SCOPE OF THE MMP PORTFOLIO

Running through 2015, the MMP Portfolio consists of:

US 5,440,749

US 5,530,890

US 5,604,915

US 5,659,703

US 5,784,584

US 5,809,336

US 6,598,148

European Counterparts and Japanese Counterparts.

### **US'336: Clocking CPU and I/O Separately.**

The MMP Portfolio is NOT limited to "high speed" microprocessors. In fact, during the past year of intense study of hundreds of various microprocessor designs, no correlation at all has been found between the speed of a microprocessor and the application of US'336. Use of US'336 is prevalent across most microprocessors ... from low speed microcontrollers to sophisticated systems on chips. Advertised advantages include: cost reduction, instant-on execution, failsafe operation, EMI reduction, and power savings. It is a modern requirement from a design for test ("DFT") perspective.

### **US'584: Multiple Instruction Fetch.**

Multiple Instruction Fetch architectures are the norm in environments where limiting power consumption is critical; eg; portable products. Various techniques can be employed to achieve the Multiple Instruction Fetch, and marketing terminology includes "VLIW," "SIMD," "MIMD," "Superscalar," etc.

### **US'148: On-Chip Oscillator and Embedded Memory.**

Shares the on-chip oscillator feature with US'336, in addition to memory covering more than majority of chip. Also includes claims pertaining to multiple CPU, array or cell implementations. The vast majority of the system on chip ("SoC") products are affected.

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Virtually every product manufactured today utilizing microprocessors or embedded processors will require an MMP Portfolio license. The Portfolio covers microprocessors and the Systems containing microprocessors.

#### IV. PORTFOLIO BACKGROUND

The TPL Group assisted Charles Moore in the development of the MMP Technology and Patents beginning 1989. Mr. Moore, also the inventor of the Forth computing language, now serves as Chief Technology Officer of the TPL Group.

Alliacense has been purpose-built by the TPL Group to implement licensing programs, and is staffed with leading licensing experts from various industries. Based on the large number of Licensees we are serving, it may be some time before you hear from us again directly. Be assured however, that this would only indicate that other companies from your business sector are occupying the crucial first-mover bandwidth ... instead of you. We therefore urge you to immediately communicate your concerns to us.

Upon request, we provide Product Reports (analyzing the relationship of a product to the Portfolio) on product lines of your choosing. Product Reports are generally available on a 48-hour turnaround. It is in our best interest to assure Product Reports are as thorough and objective as possible. We do not wish to waste your time or our own.

Enclosed for your convenience is a data disc containing the full text of all the US patents in the portfolio, as well as their file wrappers. You may access further information by requesting a username and password for the MMP Portfolio Licensing Private Website via: [mmp-licensing@tplgroup.net](mailto:mmp-licensing@tplgroup.net). General correspondence may also be sent to this address.

We have representatives in the US, Asia and Europe. We would like to meet with your Decision Makers sooner rather than later to provide an overview of the MMP Portfolio Licensing Program, and to discuss what opportunities remain available in your industry sector.

We look forward to getting acquainted and to serving you.

Sincerely,

Mac Leckrone  
President  
Alliacense

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## MEI – TPL MMP Portfolio v.2 Table of Contents (with Links)

(click the item to link to it)

### **Consumer Products**

#### **Camcorders**

- 1.0. Panasonic NV-GS Camcorder – US'336
  - 1.1. CNV-GS Series Digital Video Camcorder MPEG Schematic, Panasonic

#### **DVD & VCR Products**

- 2.0. Panasonic Portable DVD Player, DVD-LS Series – US'336
  - 2.1. Portable DVD Player LS Series Service Manual, Panasonic
  - 2.2. Portable DVD Player uP MN101C39CBY Datasheet, Panasonic
- 3.0 Panasonic PV-D4 Series DVD-VCR Deck – US'336
  - 3.1. PV-D4 Series DVD-VCR Deck Service Manual, Panasonic
- 4.0. Panasonic S27 Series DVD Player – US'336
  - 4.1. S27 Series DVD Player Service Manual, Panasonic
- 5.0. Panasonic DVD Recorder DMR Series – US'336
  - 5.1. DVD Video Recorder DMR Series, Panasonic

#### **Mobile Phones**

- 6.0.a. Panasonic GD67 GPRS Cell Phone – US'336
- 6.0.b. Panasonic GD67 GPRS Cell Phone – US'584
  - 6.1. TMS320C54x Datasheet, TI
  - 6.2. TI TCS2010 Chipset Datasheet, TI, NOV 04
  - 6.3. TI Cool Phones and Handsets, TI, NOV 04
  - 6.4. TCS 2010 Datasheet, TI, NOV 04
  - 6.5. GD87 Prod Sheet, MPhone.co.uk, NOV 04
  - 6.6. GD67 Prod Sheet, Mobiles.com.uk, NOV 04

- 7.0.a. Panasonic P900i Cellphone – US'336
- 7.0.b. Panasonic P900i Cellphone – US'584
  - 7.1. ARM926EJ-S Tech Ref Manual, ARM, 2003
  - 7.2. Panasonic P900i Data Sheet, 3G Today, NOV 04
  - 7.3. TI OMAP 1610 Block Diagram
  - 7.4. TIOMAP uPS Enhance Rich, Real Time Multimedia Features 3G Handsets, TI PR, 06 AP
  - 7.5. TMS320VC5510 Fixed-Point Digital Signal uP Data Manual, TI, JUL 04
- 8.0.a. Panasonic P2102V WCDMA Cellphone – US'336
- 8.0.b. Panasonic P2102V WCDMA Cellphone – US'584
  - 8.1. Panasonic P2102V Data Sheet, 3G Today, NOV 04
  - 8.2. TMS320VC5510 Fixed-Point Digital Signal uP Data Manual, TI, JUL 04
  - 8.3. TI OMAP 1510 Block Diagram
  - 8.4. Handsets from NTT DoCoMo Feature TI OMAP uPs, TI PR, 18 FEB 03
  - 8.5. ARM 920T Technical Reference Manual

### **Multi Media Displays**

- 9.0. Panasonic Multi Media Display – US'336
  - 9.1. Multi Media Display Service Manual, Panasonic

### **Projectors**

- 10.0. Panasonic PT-AE Series LCD Projector – US'336
  - 10.1. PT-AE Series LCD Projector IC1010 Service Manual, Panasonic, A-PC Board 4-6
- 11.0. Panasonic PT-AE Series LCD Projector – US'336
  - 11.1. PT-AE Series LCD Projector Service Manual, Panasonic

### **Receivers**

- 12.0. Panasonic In Dash Color TV-DVD – US'336
  - 12.1. In Dash Color TV-DVD Service Manual, Panasonic
- 13.0. Panasonic In Dash TV-DVD Receiver – US'336
  - 13.1. In Dash Color TV-DVD Service Manual, Panasonic

### **TV Combos**

- 14.0. Panasonic TV-DVD Combination – US'336
  - 14.1. PV Series TV-DVD Combination Service Manual, Panasonic
- 15.0. Panasonic PV-DF Series TV-DVD-VCR – US'336
  - 15.1. PV-DF Series TV-DVD-VCR Combination Service Manual, Panasonic
- 16.0. Panasonic PV-DR2714 – US'336
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**Commercial Products****Automotive**

17.0. Panasonic CQ-C9 Series MP3 WMA CD Player – US'336

17.1. CQ-C9 series Automotive MP3 WMA CD Player Service Manual,  
Panasonic, Main Board

18.0. Panasonic CD Player Receiver CQ-C1121U – US'336

18.1. Panasonic Auto CD Player Receiver CQ-C1121U Service Manual

18.2. Sanyo LC7237 EN5931

**Fax Machines**

19.0. Panasonic KX-FP145 Fax – US'336

19.1. KX-FP145 FAX w Digital Answering Service Manual, Panasonic

20.0. Panasonic Digital Cordless Answering System with Fax– US'336

20.1. Digital Answering System with Fax Service Manual, Panasonic

**Microprocessors****Microcontroller**

21.0. Panasonic OFDM Demodulation – US'336

21.1. OFDM Demodulation LSI, MN88441 Datasheet, Panasonic

**Common References:**

- Process & Environmental Variation Impacts on ASIC Timing, Zuchowski, et al., IEEE, 04
- A 7-MHz Process, Temperature and Supply Compensated Clock Oscillator in 0.25µm CMOS, Sundaresan, et al., Georgia Institute of Technology, 2002
- Enabling ARM Technology, ARM7TDMI Processor, Atmel
- An Introduction to Thumb, Advanced RISC Machines Ltd., v2.0 MAR 1995
- ARM7TDMI Technical Reference
- ARM 7TDMI Overview DVI0027B\_7\_R3